

## Electrical Characteristics

| DC Resistance: | $(1-2)=180 \Omega$ | $(5-6)=180 \Omega$ |
| :--- | :--- | :--- |
| (Maximum) | $(3-4)=48 \Omega$ | $(7-8)=48 \Omega$ |

## LOAD CONDITIONS

$\mathrm{E}(1-2)=20 \mathrm{~V}$ RMs, 2000 Hz
$\mathrm{E}(5-6)=20 \mathrm{Vrms}, 2000 \mathrm{~Hz} \quad \mathrm{Ptotal}<0.8 \mathrm{~W}$

Volt-Seconds, measured at 1.0 kHz Sqr. Wave, $\mathrm{Ipk}=100 \mathrm{~mA} \pm 10 \%$, $\mathrm{Rs}=20 \Omega: 1-2=15.0 \mathrm{mV}-\mathrm{Sec}$ Minimum $5-6=15.0 \mathrm{mV}$-Sec Minimum

Core Matching: Evidence of core matching per 33165-94 shall accompany each shipment.
Transfer Characteristics: Per Sheet 2

## These Parts Are Manufactured in Strict Compliance to MIL-STD-981.

The " $X$ " in the part number refers to the Quality Level (C, B, E, or S), see Quality

| Assurance Provisions <br> above. |
| :--- |
|  |
| DO NOT SCALE DRAWING |

UNLESS OTHERWISE SPECIFIED:
Dimensions are in inches, and tolerances are:
Fractions Decimal . $X= \pm 0.1$

| $\pm 1 / 64$ | $\begin{array}{l}x X \\ . \\ \\ \\ \\ \end{array} \mathrm{XXX}= \pm 0.010$ |
| :--- | :--- |


| $\pm 1 / 64$ | $x \times \pm= \pm 0.02$ <br> $. x \times x= \pm 0.010$ |
| :---: | :---: |
| DRAWN BY | DATE |
| Jim Allen | $06 / 07 / 04$ |

:

Angles
TITL

| FSCM | DWG. NO | REV. |
| :---: | :--- | :--- | :--- |
| 23558 |  |  |



Set Ein $=29$ V Pk-Pk (Square Wave) at 2000 Hz
Transfer Characteristics (Ic - Test with IB open circuit):
$\frac{\text { Ic }(\mathrm{ADC})}{0.0}$
Vo (VDC)
Route 1 turn Ic cable $>6^{\prime \prime}$ from core, before and after going through the core.
$+0.4 \quad 0.0310-0.2510$
$+1.0 \quad 0.0830-0.3275$
Select $\mathrm{R}_{\mathrm{o}}$ for best transfer characteristics curve for both windings. $+5.0 \quad 0.4375-0.8375$
+10 1.075-1.475
Transfer Characteristics (Ib - Test with Ic open Circuit and Ro in place):
$+20$
2.350-2.750

Set Ib to $20.0 \mathrm{~mA} \pm 0.5 \%$, $\mathrm{Vo}=1.27$ to 1.31 VDC
+30 3.625-4.025
$+40 \quad 4.900-5.300$
Note: Substitute terminals $1 \& 2$ for $\mathbf{a} \& \mathbf{b}$ and $3 \& 4$ for $\mathbf{c} \& \mathbf{d}$ in the schematic and test Ic and IB, then substitute terminals $5 \& 6$ for $\mathbf{a} \& \mathbf{b}$ and $7 \& 8$ for $\mathbf{c}$ $\& \mathbf{d}$ in the schematic and re-test Ic and Iв.

|  | UNLESS OTHERWISE SPECIFIED: <br> Dimensions are in inches, and tolerances are: |  | COAST/ACM |  |
| :---: | :---: | :---: | :---: | :---: |
|  |  |  | TITLE <br> Transformer, Current Monitor |  |
|  |  | $\pm 5^{\circ}$ | FSCM | DWG. NO |
|  | DRAWN BY | DATE | 22558 | 33165X |
| DO NOT SCALE | Jim Allen | 06/07/04 | SCALE: none | SHEET 2 OF 2 |

